

WHAT IS CLAIMED IS:

Sub 2 1. A signal processing circuit for processing a signal reproduced from a CD, comprising:

5 a CD-ROM decoder for decoding incoming CD-ROM data by using a memory; and

an anti-shock controller for causing a predetermined amount of incoming audio data to be stored in said memory, and reading and outputting the audio data from said memory, so that continuous output can be achieved even when the incoming audio data is interrupted; wherein

said CD-ROM decoder and said anti-shock controller access said memory.

15 2. The circuit according to claim 1, wherein said memory is divided into an area used for decoding the CD-ROM data, and an area where the audio data is stored.

20 3. The circuit according to claim 1, wherein said memory is entirely used for decoding the CD-ROM data or for storing the audio data.

4. The circuit according to claim 1, wherein said CD-ROM decoder and said anti-shock controller are
25 connected to said memory through a common interface, and said interface includes a selection circuit for selecting either a signal from said CD-ROM decoder or a signal from said anti-shock controller.

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